



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Result of III B.Tech II SEMESTER (R16) Supplementary Examinations, June-2022

College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Grade	Credits
16JG1A0232	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	ABSENT	0
16JG1A0232	R1632022	POWER SYSTEM ANALYSIS	ABSENT	0
16JG1A0232	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	ABSENT	0
16JG1A0232	R1632024	DATA STRUCTURES	ABSENT	0
16JG1A0427	R1632044	DIGITAL SIGNAL PROCESSING	C	3
16JG1A0436	R1632044	DIGITAL SIGNAL PROCESSING	B	3
16JG1A0452	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
16JG1A0452	R1632042	MICRO WAVE ENGINEERING	ABSENT	0
16JG1A0452	R1632044	DIGITAL SIGNAL PROCESSING	ABSENT	0
16JG1A0497	R1632044	DIGITAL SIGNAL PROCESSING	D	3
17JG1A0202	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
17JG1A0202	R1632022	POWER SYSTEM ANALYSIS	F	0
17JG1A0202	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
17JG1A0202	R1632024	DATA STRUCTURES	ABSENT	0
17JG1A0213	R1632024	DATA STRUCTURES	D	3
17JG1A0447	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
17JG1A0460	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	D	3
17JG1A0495	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
17JG1A0495	R1632044	DIGITAL SIGNAL PROCESSING	F	0
17JG1A0495	R163204A	OOPS THROUGH JAVA	F	0
17JG1A04A6	R1632044	DIGITAL SIGNAL PROCESSING	C	3
17JG1A04B4	R1632044	DIGITAL SIGNAL PROCESSING	F	0
17JG1A0566	R1632051	COMPUTER NETWORKS	F	0
17JG1A0584	R1632051	COMPUTER NETWORKS	F	0
17JG1A0584	R1632052	DATA WAREHOUSING AND MINING	ABSENT	0
17JG1A0584	R163205A	ARTIFICIAL INTELLIGENCE	ABSENT	0
17JG5A0204	R1632024	DATA STRUCTURES	F	0
18JG1A0201	R1632022	POWER SYSTEM ANALYSIS	F	0
18JG1A0221	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	D	3
18JG1A0221	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	D	3
18JG1A0222	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
18JG1A0223	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
18JG1A0401	R163204D	BIO-MEDICAL ENGINEERING	F	0
18JG1A0402	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	B	3
18JG1A0404	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
18JG1A0405	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
18JG1A0413	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
18JG1A0417	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	ABSENT	0
18JG1A0417	R1632042	MICRO WAVE ENGINEERING	ABSENT	0
18JG1A0417	R163204D	BIO-MEDICAL ENGINEERING	ABSENT	0
18JG1A0418	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	D	3
18JG1A0419	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
18JG1A0419	R1632042	MICRO WAVE ENGINEERING	F	0
18JG1A0419	R1632044	DIGITAL SIGNAL PROCESSING	F	0
18JG1A0419	R163204D	BIO-MEDICAL ENGINEERING	F	0

Htno	Subcode	Subname	Grade	Credits
18JG1A0425	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
18JG1A0433	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	D	3
18JG1A0433	R1632044	DIGITAL SIGNAL PROCESSING	F	0
18JG1A0436	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	B	3
18JG1A0444	R1632043	VLSI DESIGN	C	3
18JG1A0450	R1632044	DIGITAL SIGNAL PROCESSING	C	3
18JG1A0452	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
18JG1A0453	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	B	3
18JG1A0454	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	B	3
18JG1A0457	R1632044	DIGITAL SIGNAL PROCESSING	B	3
18JG1A0462	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	ABSENT	0
18JG1A0462	R1632042	MICRO WAVE ENGINEERING	F	0
18JG1A0462	R1632043	VLSI DESIGN	ABSENT	0
18JG1A0462	R1632044	DIGITAL SIGNAL PROCESSING	F	0
18JG1A0462	R1632046	MICRO PROCESSORS & MICRO CONTROLLERS LAB	ABSENT	0
18JG1A0462	R1632047	VLSI LAB	ABSENT	0
18JG1A0462	R1632048	DIGITAL COMMUNICATIONS LAB	ABSENT	0
18JG1A0462	R163204D	BIO-MEDICAL ENGINEERING	ABSENT	0
18JG1A0478	R1632042	MICRO WAVE ENGINEERING	D	3
18JG1A0478	R1632044	DIGITAL SIGNAL PROCESSING	F	0
18JG1A0482	R1632043	VLSI DESIGN	D	3
18JG1A0482	R1632044	DIGITAL SIGNAL PROCESSING	C	3
18JG1A04A5	R1632044	DIGITAL SIGNAL PROCESSING	C	3
18JG1A04A7	R1632043	VLSI DESIGN	ABSENT	0
18JG1A04A7	R1632044	DIGITAL SIGNAL PROCESSING	ABSENT	0
18JG1A0535	R1632051	COMPUTER NETWORKS	ABSENT	0
18JG1A0535	R1632052	DATA WAREHOUSING AND MINING	ABSENT	0
18JG1A0535	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	ABSENT	0
18JG1A0535	R1632054	SOFTWARE TESTING METHODOLOGIES	ABSENT	0
18JG1A0535	R163205A	ARTIFICIAL INTELLIGENCE	ABSENT	0
18JG1A0548	R1632051	COMPUTER NETWORKS	C	3
18JG1A0548	R1632052	DATA WAREHOUSING AND MINING	D	3
18JG1A0548	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	D	3
18JG1A0548	R1632054	SOFTWARE TESTING METHODOLOGIES	C	3
18JG1A0548	R163205A	ARTIFICIAL INTELLIGENCE	D	3
18JG1A0562	R1632049	IPR & PATENTS	COMPLETED	0
18JG1A0562	R1632051	COMPUTER NETWORKS	C	3
18JG1A0562	R1632052	DATA WAREHOUSING AND MINING	D	3
18JG1A0562	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	C	3
18JG1A0562	R1632054	SOFTWARE TESTING METHODOLOGIES	B	3
18JG1A0562	R1632056	NETWORK PROGRAMMING LAB	A	2
18JG1A0562	R1632057	SOFTWARE TESTING LAB	S	2
18JG1A0562	R1632058	DATA WAREHOUSING AND MINING LAB	A	2
18JG1A0562	R163205A	ARTIFICIAL INTELLIGENCE	B	3
18JG1A0575	R163205A	ARTIFICIAL INTELLIGENCE	F	0
18JG1A05A3	R1632052	DATA WAREHOUSING AND MINING	C	3
18JG1A1212	R1632054	SOFTWARE TESTING METHODOLOGIES	ABSENT	0
18JG1A1212	R163205A	ARTIFICIAL INTELLIGENCE	ABSENT	0
18JG1A1212	R1632122	WEB TECHNOLOGIES	F	0
18JG5A0407	R1632044	DIGITAL SIGNAL PROCESSING	F	0
19JG5A0204	R1632022	POWER SYSTEM ANALYSIS	F	0

Htno	Subcode	Subname	Grade	Credits
19JG5A0206	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	D	3
19JG5A0206	R1632024	DATA STRUCTURES	D	3
19JG5A0209	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	B	3
19JG5A0211	R1632022	POWER SYSTEM ANALYSIS	F	0
19JG5A0211	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	D	3
19JG5A0214	R1632024	DATA STRUCTURES	D	3
19JG5A0404	R1632044	DIGITAL SIGNAL PROCESSING	B	3
19JG5A0412	R1632044	DIGITAL SIGNAL PROCESSING	C	3
19JG5A0414	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
19JG5A0414	R1632044	DIGITAL SIGNAL PROCESSING	F	0

\*\*Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 29-08-2022 ]

\*\* Note:\*\*

\* -1 in the filed of externals indicates student is absent for the respective subject.

\* -2 in the filed of externals indicates student result Withheld for the respective subject.

\* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:22.08.2022

Controller of Examinations